

CURRICULUM VITAE

CHOPADE SANJAY S.

PERSONAL DETAILS

Name : **Chopade Sanjay S.**

Date of Birth : 26th June 1975

Sex : Male

Marital status : Married

Nationality : Indian

Proficiency in Languages : English, Hindi, Marathi

Permanent Address : Chopade Sanjay S.
Swatic Row House No. 3
Sai Shiv Nagar, Uttara Nagar
Nashik Road,
Nashik 422 002

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Academic Achievements :

| Exam. Passed | College/Institution | University/ Board | Year of passing | % / Div |
|------------------------|------------------------------------|----------------------------|-----------------|---------------------|
| Ph.D (Regd) | RTMU, Nagpur | Nagpur University | 2019 | -- |
| M. Tech (EDT) | CEDTI, Aurangabad | Dr. B.A.M.U. Aurangabad | Feb 2002 | 76 %/ Dist |
| B.E (Elect. & tel.) | Govt. Engg. College, Amaravati. | Amaravati University | June-1996 | 66/ I ST |
| H.S.C | J.M.V. Chandrapur | Nagpur Board | March 1992 | 84.50/Dist. |
| S.S.C | L.T.V. Chandrapur | Nagpur Board | March 1990 | 81/Dist. |

Gate Score : **84.04 Percentile**

TECHNICAL SKILL

Platform : MS WINDOWS 2000, Unix (HP-UX),
 Front End : C, C++, VHDL/VERILOG.
 8051/PIC/ARM Assembly Languages
 Tools Studied : Hardware Descriptive Languages tools likes
 Xilinx, Cadence Under HP-UX platforms.
 MATLAB, HSpice, TCAD

Professional Membership : Life Member in IETE.
 Life Member in ISTE.

WORK EXPERIENCE :

| Name of the post | Company name | Duration | Place |
|--|---|---|----------------|
| Maintenance engg. | Simplex Mills Co. Ltd. | Mar 1997 to Mar 1998 | Akola |
| Production and Quality Incharge | Poonitronics India Pvt. Ltd | May 1998 to July-1999 | Pune |
| Lecturer | Rajaram Shinde Engg. College Pedhambe, Dist Ratnagiri | Aug. 1999 to Feb 2002 | Pedhambe |
| Lecturer | Pravara Rural Engineering College Loni, Dist. Ahamadnagar | Feb 2002 to Sept 2002 | Loni |
| Lecturer | MGM College of Engineering and Technology, Navi Mumbai | Sept 2002 to Sept 2004 | Navi Mumbai |
| Asst Prof. and HOD | Pravara Rural Engineering College Loni, Dist. Ahamadnagar | Sept 2004 To May 2009 | Loni |
| Associate Prof. | Sandip Institute of Technology and Research Centre, Nashik | May 2009 to 23 Aug 2016 | Nashik |
| Head of Department, Electronics and comm., | Government Polytechnic, Malvan | 24 Aug 2016 to 31 st 2024 | Malvan |

ACHIEVEMENT:**Administrative Experience :**

1. Working as PG Co-ordinator in the Sandip Institute of Technology and Research Centre, Nashik from 19th July 2011 to till date.
2. Worked as HOD in Sandip Institute of Technology and Research Centre, nashik from 14th May 2009 to 19th July 2011.

3. Worked as HOD in Pravara Rural Engineering College, Loni from Sept 2007 to Sept 2009.
4. Worked as Project-Co-ordinator in Pravara Rural Engineering College, Loni from Sept 2004 to Sept 2007.
5. Set Microprocessor Laboratory for the newly design subject Microprocessor, Microcontroller and application, Advance microprocessor in Pravara Rural Engineering College, Loni in 2005.
6. Worked as Lab Incharge of Microprocessor Lab in Pravara Rural Engineering College, Loni from Sept 2004 to July 2007.
7. Worked as a ISTE Co-ordinator. in Pravara Rural Engineering College, Loni from may 2006 to Jan 2007.
8. Worked as Under Study in MGM College of Engineering, Navi Mumbai for the Mumbai university Examination. for the year 2003.
9. Worked as Senior Supervisor in Pravara Rural Engineering College, Loni for the Pune university Examination. for the year 2006.
10. Organized One Day Seminar on "Embedded System Design" in Pravara Rural Engineering College, Loni on
11. Organized One Day Seminar Workshop on "Pokeyoke System" in Pravara Rural Engineering College, Loni on
12. Organized Three Days Workshop on "Pokeyyoke System" in Pravara Rural Engineering College, Loni on
13. Organized Three Days Workshop on "C- Interfacing & Electro pneumatics " in Pravara Rural Engineering College, Loni on
14. Organized One Day Seminar on "Industrial Automation and Robotics" in Pravara Rural Engineering College, Loni on
15. Member of the organizing Committee for the Pravara International Conference on Emerging Trends in Engineering (PICETE) in Dec 2008.
16. Organized

CONTRIBUTION TO UNIVERSITY WORK

1. Worked as Asst. to Senior Supervisor for the May-June Exam 2006 in Pravara Rural Engineering College, Loni
2. Worked as Senior Supervisor for the Nov-Dec Exam 2006 in Pravara Rural Engineering College, Loni.
3. Organised the one workshop on "Teaching Methodology for SE (Electronics and Telecommunication Engineering/Electronics- Course 2008) in Sandip Institute of Technology and Research Centre, Nashik in July 2009.
4. Worked as Senior Supervisor for the Nov-Dec Exam 2011 in Sandip Institute of Technology and Research Centre, Nashik
5. Appointed by the university of Pune as Committee member of squad for the Nov-Dec Exam 2010.
6. Appointed by the university of Pune as LIC Committee member to visit Rasoni Collge of Engineering and Management, Pune in Feb 2012.
7. Appointed by the university of Pune as LIC Committee member to visit MIT Collge of Engineering Pune in March 2012.
8. Appointed by the university of Pune as LIC Committee member to visit MIT Collge of Engineering Pune in Sept 2013.

Other R & D Activities of College:

1. Got the Grant of **Rs. 1 Laks from BCUD University of Pune, Pune** for the project "Technology based Agriculture Information Dissemination System" in 2008.
2. Got the Grant of **Rs. 2.20 Laks from BCUD, University of Pune, Pune** for the International conference in year 2010 at SITRC, Nashik.
3. Got the Grant of **Rs. 35 thousand from BCUD, University of Pune, Pune** for the Workshop "Embedded System" in the year 2010.
4. Got the Grant of **Rs. 2 Laks from BCUD University of Pune, Pune** for the international conference held in SITRC Nashik in the year 2010

Paper Published in journal:

1. Presented Paper on "**VLSI Implemented online access of meter via GSM N/w**" at the National Conference on "Trends in Telecom Transmission : Copper to Fibre" organized by Electronics and Telecommunication of K. J Somaiya College of Engineering, Mumbai-77 on March 17 & 18, 2006.
2. Presented Paper on "**MEMS and its Application**" in the International Conference Conducted by pentagram Research Pvt Ltd at Hyderabad will be on 3rd to 7th Jan 2006.
3. Presented Paper on "**RFID**" in the International Conference (PICETE) Conducted by Pravara Rural Engineering College, Loni in December 2008.
4. Presented Paper on "**RISC Based Microcontroller using VLSI**" in the International journal (IJCIIS). July 2010 Vol. 1 No. 5 © IJCIIS Publication ISSN: 1837-7823
5. Presented Paper on "**LPG Plant Control using Multidrop Network Bus Topology with GSM Technology**" in the International journal (IJCIIS). December 2011 Vol. 2 No.12 © IJCIIS
Publication ISSN: 1837-7823
6. S.S.Chopade, P. S. Aswale, "A Low Power 90nm Technology based CMOS Digital Gates with Dual Threshold Transistor Stacking Technique", International Journal of Computer Applications (0975 8887), Volume 59 - No. 11, December 2012 page 47-51.
7. S.S.Chopade, Dr. D. V. Padole , Dr. SachinPable, "Performance Analysis of CNFET based Interconnect Drivers for Sub-threshold Circuits", International Journal of Computer Applications (0975 8887), Volume 60 - No. 4, December 2012 page 16-19.
8. S.S.Chopade, P. S. Aswale, "Comparative Study of Different Low Power Design Techniques for Reduction of Leakage Power in CMOS VLSI Circuits", International Journal of Computer Applications (0975 8887) Volume 70 - No. 11, May 2013.
9. Rajni Sharma, Sanjay Chopade, "Stability Analysis of 6T SRAM at 32 Nm Technology" International Journal of Innovative Research in Science, Engineering and Technology Vol. 3, Issue 5, May 2014 ISSN No. 2319-8753.
- 10 Sanjay S. Chopade, Dinesh V. Padole, "Dual Material Pile Gate Approach for Low Leakage Finfet", International Journal of Technology (2017) 1: 166-174 ISSN 2086-9614.
11. Sanjay S. Chopade, Dinesh V. Padole, "Comparative Analysis of 4x4 Vedic and Conventional multiplier with different adders at 32 nm in different geometrical devices", International Journal of Computer Applications (0975 – 8887) Volume 159 No.4,
February 16, 2017, **ISSN No.0975 8887**.
- 12 Sanjay S. Chopade, Dinesh V. Padole, "Comparative Analysis of 4x4 Vedic and conventional multiplier with different adders at 32 nm technology", International

Journal of Computer Applications (0975 – 8887) Volume 159 No.3, February 16, 2017 **ISSN No.**0975 8887.

- 13 S.S.Chopade, Dr. D. V. Padole, "TCAD Simulation and Analysis of Drain Current and Threshold Voltage in Single Fin and Multi-Fin FinFET", Indian Journal of Science and Technology, March 2017 ISSN (Print) : 0974-6846 ISSN (Online) : 0974-5645.

International Conferences

1. S. S Chopade, Dr. D V Padole, "Design of DG-CNFET For Reduction of Short Channel Effect Over DG MOSFET at 20nm" is presented in TENCON-2013 October-2013 IEEE conference at china.
2. S.S.Chopade, Dr. D. V. Padole, "Design of Double Gate MOSFET and FDSOI using high k material for Nano scaled Circuits" is presented in TENCON-14 October-2014 IEEE conference at Thailand.
3. S.S.Chopade, "Improved temperature and supply voltage stability of Voltage controlled oscillator", Industrial Instrumentation and Control (ICIC), 2015 IEEE International Conference on 28-30 May 2015, page 193 - 196
4. S.S.Chopade, "Performance and area scaling of 6T SRAM using SOI MOSFET at 32nm node" Communication, Information & Computing Technology (ICCICT), 2015 IEEE International Conference on 15-17 Jan. 2015 978-1-4799-5521-3
5. S.S.Chopade, Dr. D. V. Padole, "Stability Analysis of 6T SRAM Cell for Nano scale FD-SOI Technology" is presented in INDICON-14 Dec 2014 IEEE conference at Pune

Patent

1. A Patent of FDSOI Device is filed on title "FDSOI-MOSFET using high k material for Nano scaled Circuits" The controller of patents, The patent office, At Mumbai 3790/MUM/2013. Dated Dec 3 2013 in November-2013
2. **Prof. Chopade Sanjay Shriram**,: Messaging System using Tactile Module for Visually Impaired People. **Application No.:** 3791/MUM/2013, India,

Workshop Attended:

1. Attended Two-Day Workshop on "Orientation Program for Teachers" in Thadomal Shahani Engineering College Mumbai on Mumbai on 23 & 24 December 1999
2. Attended One-Day Workshop on "Advances in Microelectronics" Conducted by IEEE. On 8th September 2001.
3. Attended Two days symposium on "Emerging trends in Electronics and Telecommunication" in A.C. Patil College of Engineering Navi Mumbai, on 18th & 19th April 2003.
4. Attended One-Day Workshop on "DSP Processors: Programming and Application" Conducted by PREC Loni on 17th Jan. 2005.
5. Attended One-Day Workshop on "Teaching Methodologies for TE (E & TC / ETX) 2003 course" Conducted by PICT and university of PUNE on 21st April 2005.
6. Attended One-Day Workshop on "Teaching Methodologies for TE (E & TC / ETX) 2003 course term -II" Conducted by MAE Alandi of PUNE University on 27th December 2005.
7. Attended Two-Day Workshop on "ARM Processor" Conducted by MAAER's MIT Pune on 20 & 21 Jan. 2006.
8. Attended One-Day Workshop on "Teaching Methodologies for BE (E & TC / ETX) 2003 course" Conducted by SIT and university of PUNE on 6th February 2006.
9. Attended One-Day Workshop on "Detailing of the Project work for BE (E & TC / ETX) 2003 course PUNE University" Conducted by PREC Loni on 18th March 2006.

10. Attended One-Day Workshop on “Teaching Methodologies for term-I BE (E & TC / ETX) 2003 ” Conducted by Vidya Pratishthan’s College of Engineering Baramati on 14th July 2006
11. Attended One-Day Workshop on “Teaching Methodologies for BE (E & TC / ETX) 2003 course ” Conducted by PICT PUNE and university of PUNE on 25th September 2006.

Seminar Attended:

1. Attended One-Day Seminar on “MATLAB Application in Engineering & Research” Conducted by PREC Loni on 7th February 2006.
2. Attended One-Day Seminar on “Mobile Computing and Wireless N/w ” Conducted by PREC Loni on 14th February 2006.
3. Attended One-Day Seminar on “Credit System ” Conducted by PREC Loni on 12th March 2006.
4. Attended Conference in the University & College Educator on “Education in Values & Spirituality ” Organized from 4th to 8th June, 2006 at Academy for better World, Gyan Sarovar, Mount Abu by Education Wing, Rajayoga Education & research Foundation and Prajapita Brahma Kumaris Ishwariya Vishwa Vidyalaya, Mount Abu(Rajasthan).

STTP Course Attended:

Attended One week ISTE Short Term Training Program on Advance Microcontroller and Embedded System” on 19 to 24th Jan 2004 in VECT Vashi.

Undergraduate project Guided:

Guided : 20

Postgraduate project Guided:

Guided : 04

Other Information:

Organized International conference in 4-6 march 2010 on “**International Conference on computational Intelligence and applications**”. I have got Rs. 220000 grant from BCUD Pune.