

Faculty CV

1. Name of Faculty: Mr. Prashant Udaychand Jain
2. Present Position : Lecturer in Electronics
(Presently deputed in Examination Cell and
Held position as Controller of Examinations,
Government Polytechnic, Nashik)
3. Office Address : Controller of Examinations, Examination Cell,
Government Polytechnic Nashik,
Samangaon Road, Nashik Road, Nashik, 422101 (M.S.)
4. Mobile No. : 9860218001
5. E-Mail Id : pujain_31dec@rediffmail.com
6. Date of Birth : 31/12/1977
5. Memberships of professional organizations/Bodies:
1: Student Member of Institution of Electrical and Electronics Engineers, USA.



QUALIFICATIONS:

Sr. No.	Examination	Institute	Board/ University	% of Marks	Class Award	Remark
01	Ph.D. (Electronics Engineering)	Department of Electronics	GLA University, Mathura	--	--	Pursuing (in Final Stage)
02	M. Tech. (VLSI)	Department of Electronics	North Maharashtra University, Jalgaon	73.2	First Class	Distinction

ADDITIONAL SKILLS

CAREER DETAILS:

Sr. No.	Organization	Designation	Duration	Period	Work Done in Brief
01	Smt. S. S. Patil Institute of Technology, Chopda Dist. Jalgaon (M.S.)	Lecturer in Electronics	19/01/2004 to 10/11/2010	6Years 10 Months	•Taught various subjects like Basic Electronics, Linear integrated Circuits, VLSI design.

					<ul style="list-style-type: none"> • Arranged Industrial Visits and extra-curricular activities like Engineering's day competition.
02	Government Polytechnic, Nashik (M.S.)	Lecturer in Electronics	11/11/2010 to 27/07/2015	4 Years, 8Months	<ul style="list-style-type: none"> • Taught various subjects like Network Analysis, Control Systems, Applied Electronics, Linear integrated Circuits, VLSI design. • Developed Experimental kits from students. • Arranged Industrial Visits and extra-curricular activities like Engineering's day competition. • Worked in various positions for examinations of institution
03	Government Polytechnic, Nandurbar (M.S.)	Lecturer in Electronics	03/08/2015 to 07/06/2019	4 Years, 10Months	<ul style="list-style-type: none"> • Worked as In-Charge Head of Department in Electronics as well as in Electrical Department. • Taught various Subjects. • Developed laboratories and different experimental kits and real time based projects as Nandurbar is Tribal Area of Maharashtra state.
04	Government Polytechnic, Nashik	Lecturer in	08/06/2019	4 Years,	<ul style="list-style-type: none"> • Worked as Lecturer in

	(M.S.)	Electronics	to Till date	3Months	electronics department and taught subjects like Analog Communication and Microcontroller. •From 26/06/2019 deputed as Controller of Examinations at Examination Cell.
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SPECIALIZED TRAINING COURSES ATTENDED- (Last Two Years)

Sr. No.	Course Name	Duration	Place	Training Agency	Remarks
01	CMOS DIGITAL VLSI DESIGN	JAN-2020 TO MAR-2020	G.P.NASHIK	NPTEL	Online mode
02	RECENT TRENDS IN BIMEDICAL SIGNAL PROCESSING	23/11/2020 TO 28/11/2020	G.P.NASHIK	AICTE	Online mode
03	RESEARCH AND QUALITY PUBLICATION: PRACTICES AND TOOLS.	20/12/2020 TO 24/12/2020	G.P.NASHIK	TEQIP-III	Online mode
04	LOW POWER VLSI DESIGN	20/04/2020 TO 24/04/2020	G.P.NASHIK	NITTTR CHANDIGARH	Online mode
05	ACADEMIC LEADERSHIP ENHANCEMENT IN TECHNICAL INSTITUTIONS	04/05/2020 TO 08/05/2020	G.P.NASHIK	NITTTR CHANDIGARH	Online mode
06	8051 AND ITS APPLICATIONS	18/05/2020 TO 22/05/2020	G.P.NASHIK	NITTTR CHANDIGARH	Online mode
07	RESEARCH TRENDS IN VLSI DESIGN	25/05/2020 TO 29/05/2020	G.P.NASHIK	NITTTR CHANDIGARH	Online mode
08	NANOTECHNOLOGY FOR ELECTRONIC AND PHOTONIC DEVICES (NANODEV2020)	15/06/2020 TO 19/06/2020	G.P.NASHIK	NITTTR CHANDIGARH	Online mode
09	RECENT TRENDS IN BIMEDICAL SIGNAL PROCESSING	23/11/2020 TO 28/11/2020	G.P.NASHIK	AICTE	Online mode
10	RESEARCH AND QUALITY PUBLICATION: PRACTICES AND TOOLS.	20/12/2020 TO 24/12/2020	G.P.NASHIK	TEQIP-III	Online mode

RESEARCH ACTIVITIES

(Research Papers published in Refereed Journal Articles (Total - 01)

1. Prashant U.Jain, V. K.Tomar. (2020). Comparative Analysis of FinFET and CMOS Based 6T Static Random Access Memory Cell. International Journal of Advanced Science and Technology, 29(06), 4899 - 4909. Retrieved from <http://sersec.org/journals/index.php/IJAST/article/view/19417>
2. Design of Low Power 10T SRAM Cell in 18 nm FinFET Technology, communicated and under Review in Advances in Technology Innovations journal.(Scopus Indexed)

Presentations and Publications in Refereed International Conference Proceedings (Total -02)

1. P. U. Jain and V. K. Tomar, "FinFET Technology : As A Promising Alternatives for Conventional MOSFET Technology," 2020 International Conference on Emerging Smart Computing and Informatics (ESCI), 2020, pp. 43-47, doi: 10.1109/ESCI48226.2020.9167646.
2. Prashant U. Jain, V.K.Tomar, "Performance evaluation FinFET Based SRAM Cells at 18nm Technology", IEEE conference, Aug.2020. In press.

WORK DONE IN THE AREA OF TECHNICAL EDUCATION

1. Design FinFET Based 11T SRAM and simulated on Cadence Tool at 18nm.
2. Developed Experimental kits for laboratory.(Ex. Potentiometer as an Error Detector, Liquid level measurement using parallel plate capacitor
3. Developed real time use projects for institutions.(Ex- LED Display Board for institution)
4. Developed learning materials, question banks, MCQ sets of various subjects.
5. Prepared Lab Manuals for various subjects.

WORKSHOP/ COMPETITION ORGANISZED/ARRANGED

1. Arranged Technical Quiz competition on Engineers' Day (15th Sept. of Every Year)

PROJECT GUIDANCE

1. Guided for project like **Acrylic LED Display Board** (College Name- for GP Nandurbar)
2. Guided for projects like **Thyristor controlled Experimental Kits.**
3. Guided for projects like **Wireless Petroleum Tank Monitoring System.**
(selected in Dipex- 2013 held at Pune)
4. Guided for projects like **GSM based Multipurpose Security System.**
(selected in Dipex- 2013 held at Pune)
5. Guided for projects like **Water Purifying System and Waste Water Management.**

EXPERT LECTURES/TALKS

1. Delivered expert talk for **Career Fair-On Wheel, 2015-16**
2. Delivered expert talk for **Mechanical Measurements** in workshop arranged at Govt.

Polytechnic, Jalgaon.

3. Delivered expert lecture on **Introduction to Microprocessor** at Dr. Manoj A. Shete college of Engineering and Technology, Kasara, Dist. Thane.
4. Delivered expert lecture on **Introduction to Microprocessor and Microcontroller** at Dr. Manoj A. Shete college of Engineering and Technology, Kasara, Dist. Thane.
5. Delivered expert lecture on **Recent trends In Electronics Engineering** at Dr. Manoj A. Shete college of Engineering and Technology, Kasara, Dist. Thane.
6. Delivered expert lecture on **Advances in Measurements** at Government Polytechnic, Nashik.
7. Delivered expert lecture on topics of electronics as **Registers and Counters** at MIT World Peace University, Pune.
8. Taught Analog VLSI course to ME/M. Tech. students of Sandip Institute of Technology and Research Centre, Nashik(affiliated to Pune University)

WORK DONE AT INSTITUTI/ONORGANIZATIONAL LEVEL

1. Worked as In-charge Principal of Government Polytechnic, Nandurbar
2. Worked as In-charge Head of Department (Electronics Engg. Dept) , Govt. Polytechnic, Nandurbar.
3. Worked as In-charge Head of Department (Electrical Engg. Dept), Govt. Polytechnic, Nandurbar.
4. Worked as Officer on Special Duty(OSD) at Directorate of Technical Education Head Office, Mumbai.

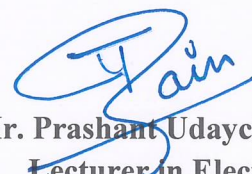
WORK DONE FOR SOCIETY

1. Arranged Blood donation camp in Govt. Polytechnic, Nandurbar.
2. Donated Blood number of times.
3. Involved in Tree plantation scheme.
4. Involved in Swachhata Abhiyan.
5. Involved in Unnat Bharat Abhiyan.
6. Motivated students about Entrepreneurship/Start up.

HONORS AND AWARDS

1. Horned by Principal Government Polytechnic, Nashik for 100% result of Courses taught (2 Times for 2 different courses)
2. Received Certificate of Appreciation for Zero error MSBTE Regional Assessment Center (paper Assessment).

Yours Sincerely,



Mr. Prashant Udaychand Jain
Lecturer in Electronics,
Government Polytechnic, Nashik