

RESUME

Name of Faculty: Dr.M.V.Patil
 Present Position: Sr.Lecturer
 Address: Nashik

4. Mobile No. : 9623757920

5. E-Mail Id : mvpatil9483@gmail.com

6. Date of Birth : 15/07/1973

QUALIFICATIONS:

Sr.	Examination	Institute	Board/	% of	Class Award
No.			University	Marks	
1	Diploma (Industrial Electronics)	SS PATIL Chopda	BTE	70	I
2	BE(Electronics)	SSVPSCOE Dhule	NMU. Jalgaon	62	I
3	ME(Digital Electronics)	RMIT COE Amravati	AMRAVATI	62	I
4	Ph.D (Electronics & Communication)	Pacific academy of higher education and research university Udaipur Rajasthan	PACIFIC UNIVERSITY UDAIPUR RAJASTHAN	7.75 (CGPA)	I

ADDITIONAL SKILLS Technical Summary/ Professional Memberships / Certifications

Simulation & Synthesis Tools : MultiSim, Orcad (PSpice), MATLAB 14.

• Simulation & Synthesis Tools . MultiSilli, Orcau (PS	plice), MATLAD 14.
Professional Society / Certification	Month / Year Certified
Information Technology Infrastructure Library (Mumbai)	July 2008
CCNA/PIX FIREWALL/CCNP(Routing & Switching) (Mumbai)	May 2007

Hardware & Networking Excel Technologies (Mumbai)	Nov 2005
Visual Basic (6.0) ASP (3.0),Oracle (8.0) (Mumbai)	Aug 1999
Indian Technical Education Society -TS(Mumbai)	June1995
Indian Technical Education Society -TVS(Mumbai)	June1995

CAREER DETAILS:

Sr. No.	Organization	Designation	Duration	Period
110.		G. T.	15/11/2010	TP:11 1
1	Government Polytechnic	Sr.Lecturer	15/11/2010	Till date
	Nashik			
2	PIIT COE	Lecturer	21/01/2004	14/11/2010
	(Navi-Mumbai)			
3	PCT COE(Mumbai)	Lecturer	6/08/2001	20/1/2004
4	PATNI Computer	Team	18/12/1996	7/08/2001
	(Mumbai)	Leader		

SPECIALIZED TRAINING COURSES ATTENDED:

Sr. No.	Title of Training Programme/ FDP	Duration of Training (From – to)	Venue	Sponsored/ Approved by	Duration in weeks (1/2/3/4)
1	Microprocessor & Embedded System	04/06/2018 to 08/06/2018	NITTTR Bhopal	NITTTR, Bhopal	1
2	Accreditation of Engineering Program	2/07/2018 to 6/07/2018	NITTTR Bhopal	NITTTR, Bhopal	1
3	Entrepreneurship Development & Industry Institute Partnership	25/03/2019 to 29/03/2019	NITTTR Bhopal	NITTTR, Bhopal	1
4	DATA SCIENCE	02/12/2019 to 7/12/2019	SIPNA COE AMRAVATI	MHRD-FDP	1
5	Internet of thing	15/04/2020	NITTTR	NITTTR	1

	(IOT)	TO 19/4/2020	CHANDIGARH		
6	ICT Based Teaching Learning	20/4/2020 to 16/05/2020	GP ,PHARMACY JALGAON	MSBTE	4
7	Technological Interventions using wireless Communication	18/05/2020 to 22/05/2020	NITTTR CHANDIGARH	NITTTR	1
8	Power Electronics	25/05/2020 to 29/05/2020	NITTTR KOLKATA	NITTTR KOLKATA	1
9	ICT based Cyber Security	8/06/2020 to 19/06/2020	NITTTR CHANDIGARH	NITTTR	2
10	Artificial Intelligence & Applications	27/08/2020 to 31/08/2020	NITTTR CHANDIGARH	NITTTR	1
11	Artificial Intelligence & Applications	27/08/2020 to 31/08/2020	NITTTR CHANDIGARH	NITTTR CHANDIGARH	1
12	Waste Technology (ATAL	12/10/2020 to 16/12/2020	ANNA UNIVERSITY	ATAL	1
13	Block Chain Technology	23/11/2020 to 16/12/2020	AMRUTVAHI COE SANGAMNER	AICTE	1
14	Industrial Internet of Thing (IIOT) (Industrial Training)	1/16/2021 to 10/06/2021	NSIC (GOVT OF INDIA)	(GOVT OF INDIA)	2
15	AI & ML using Python (Industrial Training)	24/06/2021 to 09/06/2021	NSIC (GOVT OF INDIA)	(GOVT OF INDIA)	2
16	Robotic Process Automation (Industrial Training)	02/08/2021 to 13/08/2021	NIELIT (Aurangabad)	MSBTE	2
L	1		I	I	L

RESEARCH ACTIVITIES

Mr.Mahendra Patil, Dr.Avinash Gawande, Dr.Dilendra Hiran, "Biometric Image Encryption Based on Modified Lenstra-Lenstra-Lovasz Algorithm," International Journal of Engineering and Techniques, IJET ISSN: 2395- 1303, volume5, Issue 4Aug 2019.

Mr.Mahendra Patil, Dr.Avinash Gawande, Dr.Dilendra Hiran, "Biometric Image Encryption Algorithm Based on Modified Rubik's Cube Principle," Springer, Journal of Engineering on Computational Vision and Bio-Engineering

16- May 2018, ISSN-2212-9351.

Mr.Mahendra Patil, Dr.Avinash Gawande, Dr.Dilendra Hiran, "Biometric Image Encryption Based on Chaotic Sine Map and Information Entropy", Springer, ICICI 2019, 2nd International Conference On Intelligent Data Communication Technologies and Internet Of Things, 12–13 Sept 2019, ISSN: 2367-4512

Mr.Mahendra Patil, Dr.Avinash Gawande, Dr.Dilendra Hiran, "Improved Logistic Map Based Algorithm for Biometric Image Encryption, Springer, Lecture Notes on Data Engineering and Communications Technologies book series, 01 August 2020, ISSN: 2367-4512

Working as a reviewer for the renowned International journals like

1. Technical Program Committee Member for IEEE International Region 10 Symposium TENSYMP 2022 (July 2022).

WORKSHOP/ COMPETITION ORGANISZED/ARRANGED

- 1. Coordinator for District Level Online the Research Paper Competition organized on the occasion of 75th Anniversary of Indian Independence.
- 2.Poster Paper competition organized in Department of Electronics & Telecommunication

PROJECT GUIDANCE

1. Seminar and Project work of Third Year Diploma in Electronics and Telecommunication Engineering. This Seminar and Project work has been carried out in academic year 2020-21. As a Seminar guide, I had assigned a Seminar on "Aoutonomus Car" to a candidate of Third Year Electronics and Telecommunication Engineering. On successful completion of the Seminar presentation and related Term work in ODD Semester the candidate has submitted the detailed Seminar Report.

WORK DONE AT INSTITUTI/ONORGANIZATIONAL LEVEL

- 1. Developed Lab Manuals For Mobile communicate & Industrial Electronics & application
- 2. Guiding various multidisciplinary projects at institute level. DSR Committee member at institute level.
- 3. Involved in various committees assigned by RBTE ,DTE(MS), MSBTE and Examination Cell
- 4. Additional Officer In charge (LAC/RAC) in GP Nashik

- 5. Member of Gathering & Convocation & Sports Co-coordinator for various Games
- 6. Alumni Co-coordinator For Department of Electronics & Telecommunication
- 7. Maintenance Member of CCTV –Camera At Institute Level
- 8. Worked as Co-coordinator For Marathi Din Pandharvada Celebrating In Maharashtra
- 9. Working as Member & for EAMC and visited for inspection at various Institute at M.S
- 10. Industrial Visit at AMBAD, SATPUR MIDC along with BSNL &Akashwani Kendra Nashik
- 11. Worked As member for ARC in Admission Process
- 12. Developed project kit from students for various subject

HONORS AND AWARDS:

Regional Level First Prize winner In project Competition (Autonomous Car –Project guide)

CONSULTANCY AND OTHER WORK

- 1 Worked as a Technical Expert in the Committee to decide technical specifications of Smart Class room Advasi Vikas Mandal (Z.P) of Nashik City
- 2. Worked as Paper Setter for (META-Z.P & MPSC-Exam-for Class –III Technical Post)

WORK DONE FOR SOCIETY

- 13. Performing duties in conduction of MPSC and UPSC examinations assigned by Hon. Collectorate office Nashik
- 14. Performing duties in conduction of various elections assigned by Hon. Collectorate office Nashik

SIGNATURE